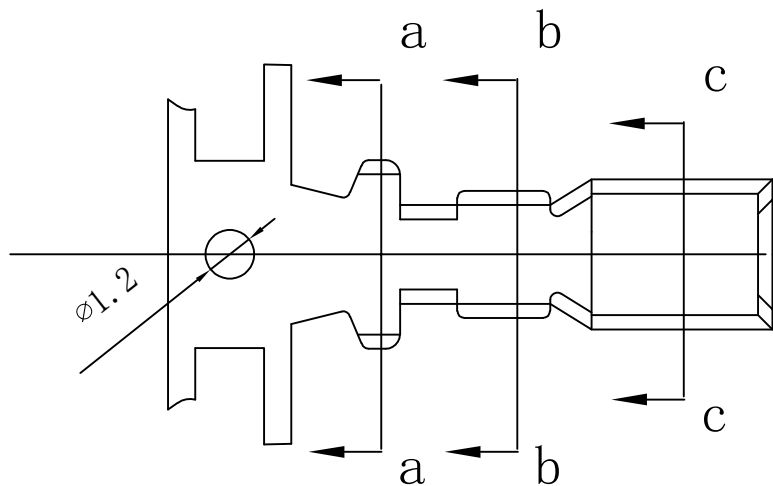
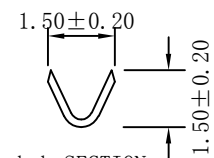
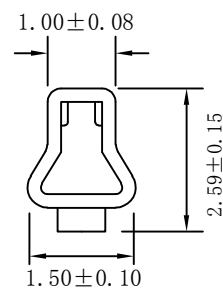
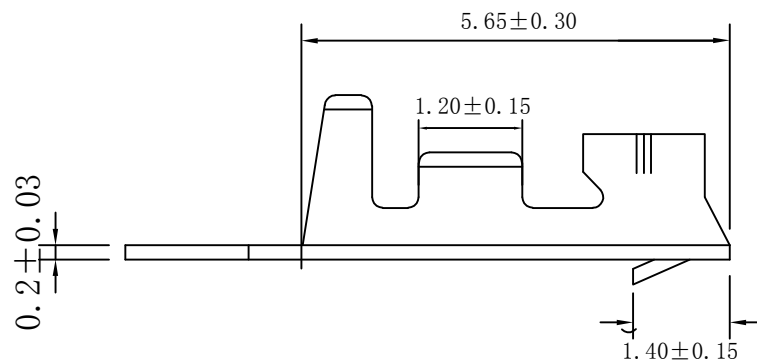


a-a SECTION



b-b SECTION



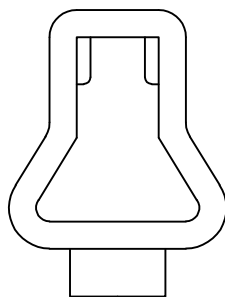
c-c SECTION

Figure 1 illustrates the PCB layout design rules for high-speed digital signals. The diagram shows a cross-section of a PCB with various layers and components. Labels indicate design rules for different sections:

- EST2001**: 羿通代码+间距
- T**: T示意端子
- P**: P磷铜, B黄铜, E合金铜
- X**: X普通脚, H高脚, E增高型脚
- 2**: 0素材, 2镀锡先冲后镀, 3镀锡先镀后冲, 4镀全模(镀后冲), 4.2镀全模先冲后镀, 5G、15G示镀金5U或15U, 1G示镀金1U
- X-**: X普通, L长, S短, U超薄
- X**: X公端, G母端, C示意90°, S示意180°

技术参数: SPECIFICATIONS

- 适用线径: AWG#24~#28
- Applicable wire: AWG#24~#28
- 额定电压: **250V DC/AC** (有效值)
- Voltage rating: 250V DC/AC (rms)
- 额定电流: **3.0A max**
- Current rating: 3.0A max
- 耐压值: **800V AC**(有效值)
- Withstand voltage: 800V AC(rms)
- 工作温度: **-25℃~+85℃**
- Working Temperature: -25℃+85℃
- 绝缘电阻: **≥1000MΩ**
- Insulation resistance ≥1000MΩ
- 接触电阻: **≤0.02Ω**
- Contact resistance ≤0.02Ω



c-c 放大2倍图



东莞市羿通实业有限公司
DONGGUAN EASTOM ELECTRONIC Co., Ltd.

料号PART NO: EST2001TPX-2X-G

晶名 TITLE: PH2.0-T母

材料:	C5191
-----	-------

单位:	MM
-----	----

包装规格:	10kg/袋
-------	--------

比例:	1 : 1
-----	-------

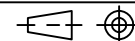
制图:	LI
-----	----

审核:	LI
-----	----

核准:	L
-----	---

日期:	2022/05/11
-----	------------

一般公差	
GENERAL TOLERANCE	
X. X	± 0.30
X. XX	± 0.20
ANGLE	$+1^\circ$



EST

6

5

4

3

2

1

EST